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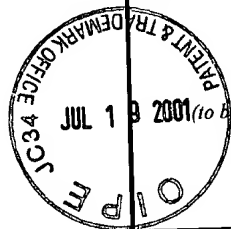
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TRANSMITTAL FORM

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| Application Number | 09/412,512 |
| Filing Date | October 5, 1999 |
| First Named Inventor | Shunpei YAMAZAKI |
| Group Art Unit | 2812 |
| Examiner Name | R. Booth |
| Attorney Docket Number | 740756-2046 |

Total Number of Pages in This Submission

ENCLOSURES (check all that apply)

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| <input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input checked="" type="checkbox"/> Amendment / Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53 | <input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) | <input type="checkbox"/> After Allowance Communication to Group |
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| Firm Or Individual name | Eric J. Robinson Nixon Peabody LLP 8180 Greensboro Drive Suite 800 McLean, VA 22012 |
| Signature | |
| Date | July 17, 2001 |

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Docket No. 740756-2046

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application:)
Shunpei YAMAZAKI)
Application No.: 09/412,512)
Filed: October 5, 1999)
For: SEMICONDUCTOR DEVICE INCLUDING)
SEMICONDUCTOR CIRCUIT MADE FROM)
SEMICONDUCTOR ELEMENT AND)
MANUFACTURING METHOD THEREOF)

Art Unit: 2812

Examiner: R. Booth

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AMENDMENT

Honorable Commissioner of Patents
Washington, D.C. 20231

Sir:

Responsive to the Office Action dated April 17, 2001, the following amendments and remarks are submitted in connection with the above-identified application.

IN THE CLAIMS:

Please amend claims 14-19 as follows. Attached hereto is a marked-up copy of the amended claims.

14. (Amended) A method for manufacturing semiconductor device comprising a semiconductor circuit, said method comprising the steps of:
- forming a base film on a plastic substrate;
 - forming a semiconductor film through a sputtering method on the base film; and
 - crystallizing the semiconductor film to form a crystalline semiconductor film,
- wherein an inert gas is used as a sputtering gas in the sputtering method, said inert gas being at least one gas selected from the group consisting of Ar, He, Ne and N.